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CMOS high speed analog circuits, HF characteristics of CMOS Transistors and integrated passive elements, low noise amplifier, mixer, VCO, prescaler, power amplifier, transceiver as complete system. An overview and a fundamental analysis of the building blocks of a Power Management ASIC (PMU) and their usage in the system is given.

[D/A Converters \(DAC\) Analog Devices](#)

Each comparator produces a 1 when its analog input voltage is higher than the reference voltage applied to it. Otherwise, the comparator output is 0. Thus, if the analog input is between $V_X/4$ and $V_X/5$, comparators X 1 through X 4 produce 1s and the remaining comparators produce 0s. The point where the code changes from ones to zeros is the ...